

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21**  
**III B.TECH II SEMESTER - SEC- A**

**Class In Charge: Dr. Sourabh Paul**

**With effect from: 01/03/2021**

**Class Room :EB-214**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	Technical Training		Aptitude		OE-II	IOT Lab/Virtual Lab		
TUE	VLSI	CCL Lab			OE-II	Technical Training	MC-IV	
WED	EWW	IOT	Verbal		OE-II	VLSI	DE-II	Seminar
THU	IOT Lab/Virtual Lab				DE-III	EWW	IOT	VLSI
FRI	DE-II	IOT	VLSI		DE-III	DE-II	EWW	GATE
SAT	IOT	DE-II	EWW		DE-III	MC-IV	Library	DE-III

<b>THEORY</b>			
	<b>CODE</b>	<b>SUBJECT</b>	<b>NAME OF STAFF MEMBER</b>
IOT	18ECE111	Internet of Things	Dr. S. Rajasekaran
EWW	18ECE112	Electromagnetic Waves and Waveguides	Dr. Sambhudutta Nanda
VLSI	18ECE113	VLSI Design	Dr. Kumar Manoj
DE-II	18ECE4M05	Discipline Elective – II (Computer Architecture)-MOOC's	Dr. P. Sarwesh
DE-III	18ECE406, 18ECE407, 18ECE408, 18ECE410, 18ECE409	Discipline Elective- III (Fiber Optic Communication, Embedded Systems, Information Theory and Coding, DSP Architectures, FPGA based System Design)	FOC (Dr. Sunil Kumar), ES (Dr. Gajendra Sharma), ITC (Dr. Avik Banerjee), DSP Architectures (Dr. Sourabh Paul, Dr. Ravi Tiwari) , FPGA based System Design (Mr. Vinay Kumar Tiwari)
		Aptitude	Mr. Balaganesh
		Verbal	Mr. Rajesh Arokiasamy
OE-II		Open Elective – II	
MC-IV		Mandatory Course –IV (Indian Constitution)	Mr. G. Subbarao
		GATE	Dr. Sumit Gupta
		Seminar	Dr. Gajendra Sharma
<b>Technical Training</b>			
CCL Lab	18ENG202	Corporate Communication Laboratory	Dr. K. Lakshmi Devi/ Dr. Vikas
IOT Lab	18ECE209	Internet of Things Laboratory	Dr. Satrugan Kumar (MON)/ Dr. Sumit Gupta (MON)/ Dr. Pradosh Ranjan Sahoo (THU)/ Dr. Soumya Ranjan Mahapatro (THU)/ Mr. Ramesh Kumar R (MON & THU)
VL		Virtual Laboratory (VLSI Design)	Dr. Kumar Manoj (MON & THU)/ Dr. Chandramauleshwar Ro (THU)/ Mr. Vinay Kumar Tiwari (MON)/ Mr. T. Manivannan (MON & THU)

**Time Table coördinator**

**HOD**

**Principal**

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21**  
**III B.TECH II SEMESTER - SEC- B**

**Class In Charge: Dr. Sankata Bhanjan Prusty**

**With effect from: 01/03/2021**

**Class Room :EB-213**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10-12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	EWV	CCL Lab			OE-II	IOT	VLSI	DE-II
TUE	VLSI	DE-II	Aptitude		OE-II	IOT Lab/Virtual Lab		
WED	IOT Lab/Virtual Lab				OE-II	IOT	MC-IV	Seminar
THU	Technical Training		IOT		DE-III	DE-II	EWV	MC-IV
FRI	Verbal	EWV	IOT		DE-III	VLSI	GATE	Library
SAT	EWV	VLSI	DE-II		DE-III	Technical Training		DE-III

<b>THEORY</b>			
	CODE	SUBJECT	NAME OF STAFF MEMBER
IOT	18ECE111	Internet of Things	Dr. Sankata Bhanjan Prusty
EWV	18ECE112	Electromagnetic Waves and Waveguides	Dr. Avik Banerjee
VLSI	18ECE113	VLSI Design	Dr. Chandramauleshwar Ro
DE-II	18ECE4M05	Discipline Elective – II (Computer Architecture) -MOOC's	Ms. Akshatha S
DE-III	18ECE406, 18ECE407, 18ECE408, 18ECE410, 18ECE409	Discipline Elective- III (Fiber Optic Communication, Embedded Systems, Information Theory and Coding, DSP Architectures, FPGA based System Design)	FOC (Dr. Sunil Kumar), ES (Dr. Gajendra Sharma), ITC (Dr. Avik Banerjee), DSP Architectures (Dr. Sourabh Paul, Dr. Ravi Tiwari), FPGA based System Design (Mr. Vinay Kumar Tiwari)
		Aptitude	Mr. Balaganesh
		Verbal	Ms. TD. Ashwini
OE-II		Open Elective – II	
MC-IV		Mandatory Course -IV	Mr. R. Kiran Kumar
		GATE	Dr. Thamizharasan. S
		Seminar	Dr. Brijesh Kumar Singh
<b>Technical Training</b>			
CCL Lab	18ENG202	Corporate Communication Laboratory	Dr. Rosalia H Bonjour/ Dr. Suraj Nandkumar Dhupal
IOT Lab	18ECE209	Internet of Things Laboratory	Dr. Velmani Ramasamy (WED)/ Dr. Sumit Gupta (TUE)/ Dr. Soumya Ranjan Mahapatro (TUE & WED) Mrs. T. Puviyarasi (TUE & WED)
VL		Virtual Laboratory (VLSI Design)	Dr. Chandramauleshwar Ro (TUE & WED)/ Mr. Vinay Kumar Tiwari (TUE & WED) Mr. V. Navaneetha Krishnan (TUE & WED)

**Time Table coordinator**

**HOD**

**Principal**

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21**  
**III B.TECH II SEMESTER - SEC- C**

**Class In Charge: Dr. Jitendra Bahadur**

**With effect from: 01/03/2021**

**Class Room :EB-212**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10-12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	IOT	Verbal	Aptitude		OE-II	DE-II	VLSI	MC-IV
TUE	IOT Lab/Virtual Lab				OE-II	Technical Training		DE-II
WED	EWW	VLSI	IOT		OE-II	IOT Lab/Virtual Lab		
THU	EWW	VLSI	DE-II		DE-III	IOT	MC-IV	Library
FRI	VLSI	EWW	DE-II		DE-III	IOT	GATE	Seminar
SAT	Technical Training		EWW		DE-III	CCL Lab		DE-III

<b>THEORY</b>			
	CODE	SUBJECT	NAME OF STAFF MEMBER
IOT	18ECE111	Internet of Things	Dr. Satrugan Kumar
EWW	18ECE112	Electromagnetic Waves and Waveguides	Dr. Ranjan Kumar Mahapatra
VLSI	18ECE113	VLSI Design	Dr. Upendra Kumar Verma
DE-II	18ECE4M05	Discipline Elective – II (Computer Architecture) -MOOC's	Dr. Shanmugakumar M
DE-III	18ECE406, 18ECE407, 18ECE408, 18ECE410, 18ECE409	Discipline Elective- III (Fiber Optic Communication, Embedded Systems, Information Theory and Coding, DSP Architectures, FPGA based System Design)	FOC (Dr. Sunil Kumar), ES (Dr. Gajendra Sharma), ITC (Dr. Avik Banerjee), DSP Architectures (Dr. Sourabh Paul, Dr. Ravi Tiwari) , FPGA based System Design (Mr. Vinay Kumar Tiwari)
		Aptitude	Mr. Tipu Rahaman S
		Verbal	Mr. Nanda Kumar P
OE-II		Open Elective – II	
MC-IV		Mandatory Course -IV	Mr. V. Prakash Kumar
		GATE	Dr. Chandramauleshwar Roy
		Seminar	Dr. Satrugan Kumar
<b>Technical Training</b>			
CCL Lab	18ENG202	Corporate Communication Laboratory	Dr. Akhil Kumar Jha/ Sarah
IOT Lab	18ECE209	Internet of Things Laboratory	Dr. Satrugan Kumar (TUE & WED)/ Dr. Sritama Roy (TUE)/ Dr. Tasher Ali Sheikh (WED)/ Mr. B. Subbarayudu (TUE & WED)
VL		Virtual Laboratory (VLSI Design)	Dr. Upendra Kumar Verma (TUE & WED)/ Dr. Indrasen Singh (TUE)/ Dr. Sunil Kumar (WED) / Mr. E. Sathiyarayanan (TUE & WED)

**Time Table coordinator**

**HOD**

**Principal**

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21**  
**III B.TECH II SEMESTER - SEC- D**

**Class In Charge: Dr. Ranjan Kumar Mahapatra**

**With effect from: 01/03/2021**

**Class Room :EB-211**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10-12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	IOT	Aptitude	EWW		OE-II	VLSI	DE-II	Seminar
TUE	Technical Training		IOT		OE-II	EWW	VLSI	MC-IV
WED	VLSI	Verbal	IOT		OE-II	CCL Lab		GATE
THU	EWW	MC-IV	DE-II		DE-III	IOT Lab/Virtual Lab		
FRI	IOT Lab/Virtual Lab				DE-III	IOT	DE-II	Library
SAT	EWW	VLSI	DE-II		DE-III	Technical Training		DE-III

<b>THEORY</b>			
	CODE	SUBJECT	NAME OF STAFF MEMBER
IOT	18ECE111	Internet of Things	Dr. Bibhuti Bhusan Pradhan
EWW	18ECE112	Electromagnetic Waves and Waveguides	Dr. Ravi Shankar
VLSI	18ECE113	VLSI Design	Dr. Sankar Prasad Bag
DE-II	18ECE4M05	Discipline Elective – II (Computer Architecture) -MOOC's	Ms. S. Ayesha Tanveer
DE-III	18ECE406, 18ECE407, 18ECE408, 18ECE410, 18ECE409	Discipline Elective- III (Fiber Optic Communication, Embedded Systems, Information Theory and Coding, DSP Architectures, FPGA based System Design)	FOC (Dr. Sunil Kumar), ES (Dr. Gajendra Sharma), ITC (Dr. Avik Banerjee), DSP Architectures (Dr. Sourabh Paul, Dr. Ravi Tiwari) , FPGA based System Design (Mr. Vinay Kumar Tiwari)
		Aptitude	Mr. Tipu Rahaman S
		Verbal	Mr. Rajesh Arokiasamy
OE-II		Open Elective – II	
MC-IV		Mandatory Course -IV	Mr. B. Subbarayudu
		GATE	Dr. Dheeren Ku Mahapatra
		Seminar	Dr. Veeraiyah Thangasamy
<b>Technical Training</b>			
CCL Lab	18ENG202	Corporate Communication Laboratory	Dr. Akhil Kumar Jha/ Dr. K. Lakshmi Devi
IOT Lab	18ECE209	Internet of Things Laboratory	Dr. Bibhuti Bhusan Pradhan (THU & FRI)/ Dr. Tasher Ali Sheikh (THU & FRI)/ Ms. S. Ayesha Tanveer (THU & FRI)
VL		Virtual Laboratory (VLSI Design)	Dr. Sankar Prasad Bag (THU & FRI) /Dr. Upendra Kumar Verma (THU)/ Dr. Indrasen Singh (FRI) /Mr. S. Tamil Selvan (THU & FRI)

**Time Table coordinator**

**HOD**

**Principal**

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
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**III B.TECH II SEMESTER - SEC- E**

**Class In Charge: Dr. Ravi Tiwari**

**With effect from: 01/03/2021**

**Class Room : EB-210**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10-12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	IOT Lab/Virtual Lab				OE-II	DE-II	DE-III	Library
TUE	EWW	Aptitude	DE-II		OE-II	IOT	VLSI	MC-IV
WED	Technical Training		EWW		OE-II	DE-II	VLSI	MC-IV
THU	Verbal	IOT	EWW		DE-III	Technical Training		Gate
FRI	EWW	VLSI	IOT		DE-III	CCL Lab		Seminar
SAT	VLSI	IOT	DE-II		DE-III	IOT Lab/Virtual Lab		

<b>THEORY</b>			
	CODE	SUBJECT	NAME OF STAFF MEMBER
IOT	18ECE111	Internet of Things	Dr. Sharwan Ram
EWW	18ECE112	Electromagnetic Waves and Waveguides	Dr. Brijesh Kumar Singh
VLSI	18ECE113	VLSI Design	Dr. Ritesh Kumar Jaiswal
DE-II	18ECE4M05	Discipline Elective – II (Computer Architecture) -MOOC's	Dr. Prashant Upadhyay
DE-III	18ECE406, 18ECE407, 18ECE408, 18ECE410, 18ECE409	Discipline Elective- III (Fiber Optic Communication, Embedded Systems, Information Theory and Coding, DSP Architectures, FPGA based System Design)	FOC (Dr. Sunil Kumar), ES (Dr. Gajendra Sharma), ITC (Dr. Avik Banerjee), DSP Architectures (Dr. Sourabh Paul, Dr. Ravi Tiwari) , FPGA based System Design (Mr. Vinay Kumar Tiwari)
		Aptitude	Mr. Bala Ganesh S
		Verbal	Mrs. Ashwini T. D
OE-II		Open Elective – II	
MC-IV		Mandatory Course -IV	Mr. E. Ramesh
		GATE	Dr. Soumya Ranjan Mahapatro
		Seminar	Dr. Sankata Bhanjan Prusty
<b>Technical Training</b>			
CCL Lab	18ENG202	Corporate Communication Laboratory	Dr. Vikas/ Dr. Madhupama
IOT Lab	18ECE209	Internet of Things Laboratory	Dr. Sharwan Ram (MON & SAT)/ Dr. Sambhudutta Nanda (MON & SAT)/ Mr. G. Mahammed Rafi (MON & SAT)
VL		Virtual Laboratory (VLSI Design)	Dr. Ritesh Kumar Jaiswal (MON & SAT)/ Dr. Sankar Prasad Bag (SAT)/ Dr. Sunil Kumar (MON)/ Ms. Akshatha S (MON & SAT)

Time Table coordinator

HOD

Principal

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21**  
**III B.TECH II SEMESTER - SEC- F**

**Class In Charge: Dr. Dheeren Ku Mahapatra**

**With effect from: 01/03/2021**

**Class Room :EB-217**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10-12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	VLSI	Aptitude	DE-II		OE-II	EWV	IOT	MC-IV
TUE	Technical Training		Verbal		OE-II	DE-II	VLSI	MC-IV
WED	DE-II	CCL Lab			OE-II	Technical Training		Library
THU	EWV	IOT	DE-II		DE-III	VLSI	Gate	Seminar
FRI	EWV	IOT	VLSI		DE-III	IOT Lab/Virtual Lab		
SAT	IOT Lab/Virtual Lab				DE-III	IOT	EWV	DE-III

<b>THEORY</b>			
	CODE	SUBJECT	NAME OF STAFF MEMBER
IOT	18ECE111	Internet of Things	Dr. Velmani Ramasamy
EWV	18ECE112	Electromagnetic Waves and Waveguides	Dr. Raghavendra Pal
VLSI	18ECE113	VLSI Design	Dr. Nilesh Anand Srivastava
DE-II	18ECE4M05	Discipline Elective – II (Computer Architecture) -MOOC's	Dr. Prashant Upadhyay
DE-III	18ECE406, 18ECE407, 18ECE408, 18ECE410, 18ECE409	Discipline Elective- III (Fiber Optic Communication, Embedded Systems, Information Theory and Coding, DSP Architectures, FPGA based System Design)	FOC (Dr. Sunil Kumar), ES (Dr. Gajendra Sharma), ITC (Dr. Avik Banerjee), DSP Architectures (Dr. Sourabh Paul, Dr. Ravi Tiwari) , FPGA based System Design (Mr. Vinay Kumar Tiwari)
		Aptitude	Mr. Bala Ganesh S
		Verbal	Mr. Nanda Kumar P
OE-II		Open Elective – II	
MC-IV		Mandatory Course -IV	Mr. S. Tamil Selvan
		GATE	Dr. Shanidul Hoque
		Seminar	Dr. Jyoti Prasanna Patra
<b>Technical Training</b>			
CCL Lab	18ENG202	Corporate Communication Laboratory	Dr. Rosalia H Bonjour/ Dr. Vikas
IOT Lab	18ECE209	Internet of Things Laboratory	Dr. Velmani Ramasamy (FRI & SAT)/ Dr. Sumit Gupta (SAT)/ Dr. Pradosh Ranjan Sahoo (FRI)/ Mr. R. Kiran Kumar (FRI & SAT)
VL		Virtual Laboratory (VLSI Design)	Dr. Nilesh Anand Srivastava (FRI & SAT)/ Dr. Sunil Kumar (FRI)/ Dr. Indrasen Singh (SAT)/ Mr. V. Prakash Kumar (FRI & SAT)

**Time Table coordinator**

**HOD**

**Principal**